

### Open-Q 835 HDK Display B2B Connector Pinout

| Pad # | Net Name          | Pad # | Net Name          |
|-------|-------------------|-------|-------------------|
| 1     | No Net            | 51    | LCD1_RESET_N      |
| 2     | GND               | 52    | MIPI_DSI0_LANE1_N |
| 3     | BLSP_TP_3         | 53    | No Net            |
| 4     | BLO_EN            | 54    | MIPI_DSI0_LANE1_P |
| 5     | BLSP_TP_2         | 55    | VDDIO_TP_1P8      |
| 6     | LPG_WLED          | 56    | GND               |
| 7     | BLSP_TP_1         | 57    | VREG_S4A_1P8      |
| 8     | GND               | 58    | MIPI_DSI0_CLK_N   |
| 9     | BLSP_TP_0         | 59    | No Net            |
| 10    | No Net            | 60    | MIPI_DSI0_CLK_P   |
| 11    | BLSP9_SPI_CLK     | 61    | NetC1304_1        |
| 12    | SWIRE             | 62    | GND               |
| 13    | QUA_MI2S_DATA1    | 63    | CABC              |
| 14    | GND               | 64    | MIPI_DSI0_LANE2_N |
| 15    | TP_RST_N          | 65    | GND               |
| 16    | MIPI_DSI1_LANE3_P | 66    | MIPI_DSI0_LANE2_P |
| 17    | HRM_INT           | 67    | VDISP_P_OUT       |
| 18    | MIPI_DSI1_LANE3_N | 68    | GND               |
| 19    | TS1_RESET_N       | 69    | VDISP_M_OUT       |
| 20    | GND               | 70    | MIPI_DSI0_LANE3_N |
| 21    | FP_SPI_MOSI       | 71    | HDMI_RX_IRQ3      |
| 22    | MIPI_DSI1_LANE2_P | 72    | MIPI_DSI0_LANE3_P |
| 23    | NFC_DWL_REQ       | 73    | No Net            |
| 24    | MIPI_DSI1_LANE2_N | 74    | GND               |
| 25    | DISP_IFC_2        | 75    | SEC_MI2S_SCK      |
| 26    | GND               | 76    | NFC_I2C6_SCL      |
| 27    | VREG_L14A_1P8     | 77    | SEC_MI2S_DATA0    |
| 28    | MIPI_DSI1_CLK_N   | 78    | NFC_I2C6_SDA      |
| 29    | GND               | 79    | No Net            |
| 30    | MIPI_DSI1_CLK_P   | 80    | GND               |
| 31    | VREG_APIX_1P8     | 81    | NetC1306_1        |
| 32    | GND               | 82    | BL1_EN            |
| 33    | APIX3_STATUS1     | 83    | NetC1306_1        |
| 34    | MIPI_DSI1_LANE1_P | 84    | LPG_WLED2         |
| 35    | APIX3_RESET_1     | 85    | GND               |
| 36    | MIPI_DSI1_LANE1_N | 86    | GND               |
| 37    | No Net            | 87    | GND               |
| 38    | GND               | 88    | VREG_WLED         |
| 39    | TP_INT_N          | 89    | DC_IN_12V         |
| 40    | MIPI_DSI1_LANE0_N | 90    | WLED_SINK1        |
| 41    | No Net            | 91    | NetC1309_1        |
| 42    | MIPI_DSI1_LANE0_P | 92    | WLED_SINK2        |
| 43    | No Net            | 93    | NetC1309_1        |

**Open-Q 835 HDK Display B2B Connector Pinout**

| <b>Pad #</b> | <b>Net Name</b>   | <b>Pad #</b> | <b>Net Name</b> |
|--------------|-------------------|--------------|-----------------|
| 44           | GND               | 94           | WLED_SINK3      |
| 45           | DISP_IFC_1        | 95           | NetC1309_1      |
| 46           | MIPI_DSIO_LANE0_N | 96           | WLED_SINK4      |
| 47           | DISP_IFC_0        | 97           | GND             |
| 48           | MIPI_DSIO_LANE0_P | 98           | VPH_PWR         |
| 49           | DISP_IFC_3        | 99           | GND             |
| 50           | GND               | 100          | VPH_PWR         |